



**Electrical Characteristics** ( $T_J = 25^\circ\text{C}$  unless otherwise specified)

Symbol Min. Max. Unit Test Conditions

**Off Characteristics**

$V_{(Min)}$

[Redacted]

[Redacted]

[Redacted]

[Redacted]







### Test Circuit



Figure 1: Gate Charge Test Circuit & Waveform

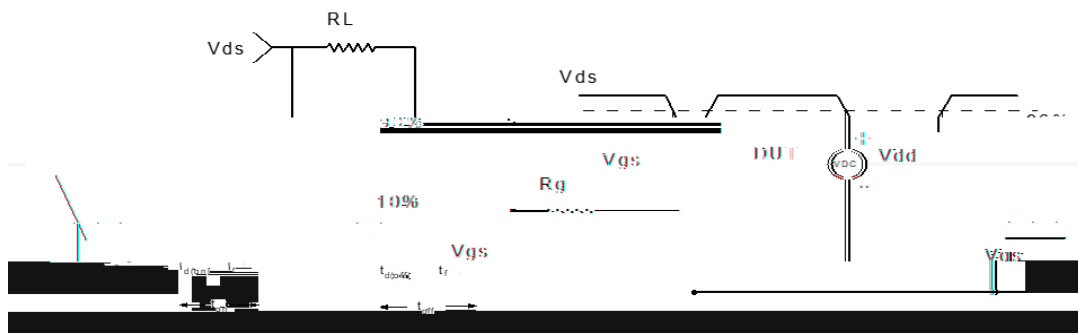


Figure 2: Resistive Switching Test Circuit & Waveform

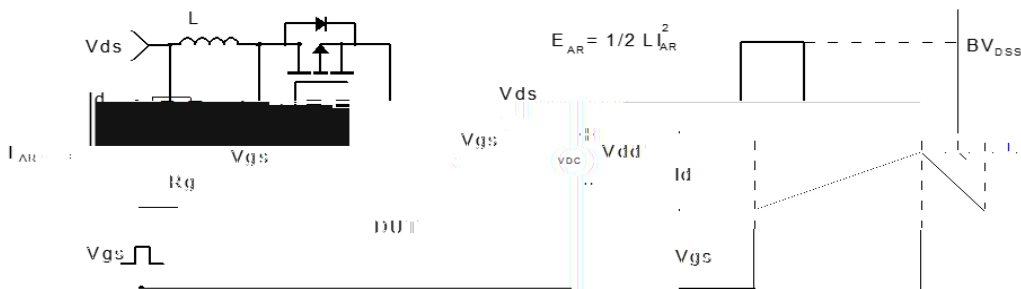


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

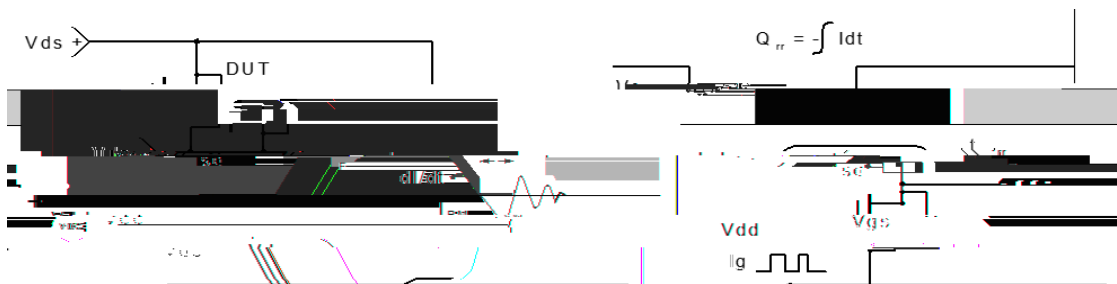


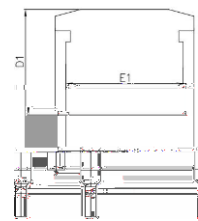
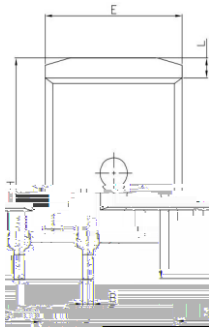
Figure 4: Diode Recovery Test Circuit & Waveform





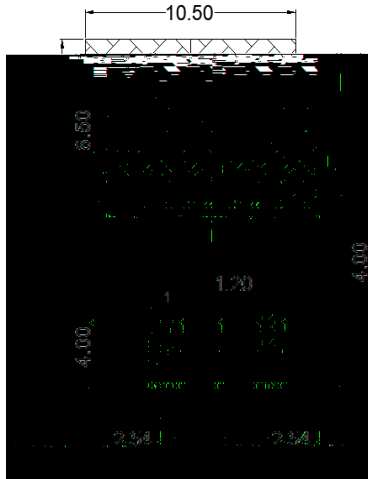
# Package Mechanical Data(TO-263 -3L)

## Package Outline



SYMBOL	DIMENSIONS		
	MIN	NOM	MAX
A	4.2	4.5	4.7
B	8.0	7.4	8.3
C	0.4	0.38	0.46
C2	0.2	0.12	0.13
D	5.0	5.0	5.1
E	7.9	7.4	7.65
F	0.1	0.0	0.2
F1	8.2	8.6	8.3
G	5.0	5.0	5.2
H	4.7	5.0	5.3

Land Pattern  
(Only for Reference)



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